

Docket No.: 60188-616



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
: :
Masayoshi TOJIMA, et al. : Confirmation Number: 7624
: :
Serial No.: 10/624,598 : Group Art Unit: 2186
: :
Filed: July 23, 2003 : Examiner: To be Assigned
: :
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OFR
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Foreign Priority information is incorrect. Attached is a copy of the Declaration and Power of Attorney Document, which evidences that the Foreign Priority information should read: **JAPAN 2002-229452 August 7, 2002**. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/624,598	07/23/2003	2186	1066	60188-616	7	12	1

CONFIRMATION NO. 7624

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FILING RECEIPT



OC000000010875405

Date Mailed: 09/15/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-229452 07/08/2002

August 7, 2002

If Required, Foreign Filing License Granted: 09/12/2003

Projected Publication Date: 01/08/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit device

Preliminary Class

711

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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